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(54) Franking machine with digital printer.

(57) A franking machine is disclosed in which a digital printing device (15) such as a thermal printer or ink jet printer is connected to accounting circuits (10) of the franking machine so as to be controlled thereby to print franking impressions. In order to prevent unauthorised control of the printing device (15) by application of external print signals, print data signals output by the accounting circuit (10) are encrypted by logically combining (26) the signals with pseudo random strings (23) of binary bits. At the printing device (15) the encrypted print data signals are decrypted by logically combining (29) the encrypted print data signals with a corresponding pseudo random string (27) of binary bits to provide print signals to control the printing device. Connections (18) between the decryption circuit (21) and the print head (15) are protected from unauthorised access.

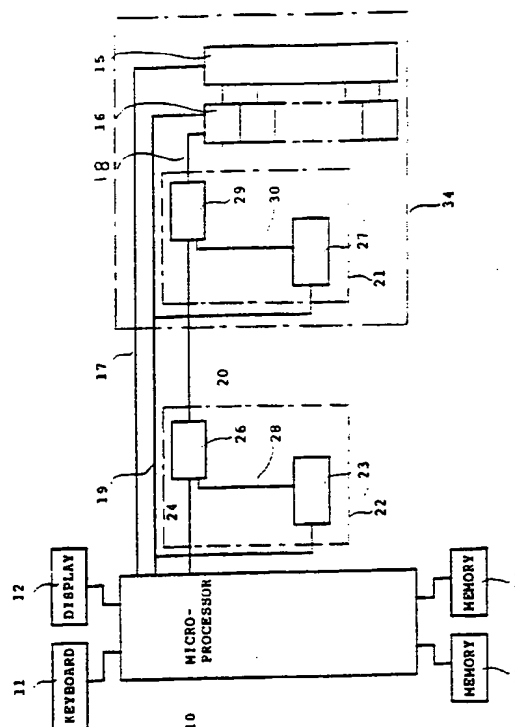


FIGURE 1

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This invention relates to franking machines in which a digital printing device is utilised to print a franking impression and in particular to providing security for print data signals which control operation of the printing device.

In known franking machines which are currently in use for franking mail items to indicate that postage charges in respect of those items has been accounted for, the printing of the franking impression is carried out by means of a print drum which carries a print die to print the fixed pattern of the impression and carries print wheels to print variable information. The variable information includes the value of the postage charge for the item and the date of franking the item. The print wheels each have a series of type characters on the periphery thereof and are settable to locate a selected one of the characters in an operative printing position whereby printing of the desired postage charge and date is effected. The print wheels are set to the required positions by mechanisms operated either directly by value setting levers or thumb wheels operated by a user of the franking machine or by stepper motors controlled by electrical drive signals in dependence upon positioning of thumb wheels or operation of keys of a keyboard by a user of the machine. The positions of the levers or thumb wheels are sensed by encoders to provide electrical signals representing a selected postage value to electronic accounting circuits to enable accounting functions to be performed in relation to the selected postage value. In franking machines having a postage meter which operates in a pre-payment mode, a descending register in the meter stores a value of credit available for use in franking mail items with postage charge values and each time a mail item is franked the credit value in the descending register is decremented by the amount of the postage charge value for that item. The postage meter also includes an ascending register to store an accumulated value of postage charge used by the meter in franking mail items and is incremented by the value of postage charge as each item is franked. A further register stores a count of the number of items franked by the machine and is incremented by one each time an item is franked. Accordingly the accounting information stored in the registers provides a record of the postage used in franking mail items and the amount of credit which remains available for use in franking mail items. As is well known in franking machines the electronic accounting circuits are housed in a secure housing to inhibit unauthorised and fraudulent attempts to tamper with the accounting data and with operation of the postage meter. While the surface of the print drum and the selected type characters of the print wheels are exposed, the mechanisms for setting the print wheels are contained within the print drum and extend through an arbour for the drum into the secure housing of the meter. Accordingly the setting mechanisms are protected from unauthorised tam-

pering and the setting mechanisms are so constructed as to prevent changing of the setting of the print wheels by applying force directly to the print wheels. Thus the known mechanical printing devices used in franking machines prevent unauthorised re-setting of the printing elements.

In franking machines currently being developed, it is desired to use non-mechanical digital printing devices such as ink jet print heads or thermal print heads operated directly by electrical signals which define the information both fixed and variable to be printed by the printing device to constitute the franking impression. Such printing heads need to be connected by electrical conductors to the accounting and control circuits of the franking machine in order to receive the electrical signals which are to control and selectively operate the print head. Accordingly it is necessary to prevent unauthorised application of electrical signals on these connecting conductors or to a print data signal input of the printing device resulting in operation of the printing device to print a franking impression.

According to the invention a franking machine includes electronic accounting and control circuits and a digital printing device operable by print data signals comprising binary bits generated by the accounting and control circuits to print franking impressions including a representation of a postage charge means to transmit the print data signals from the accounting and control circuits, to the digital printing device including encryption means at the accounting and control circuits comprising a first generator to generate a first pseudo random string of binary bits and means to logically combine successive bits of the first pseudo random string with bits of the print data signals to produce encrypted print data signals and decryption means at the printing device comprising a second generator to generate a second pseudo random string of binary bits corresponding to said first pseudo random string of binary bits and means to combine successive bits of the second pseudo random string of binary bits with bits of the encrypted print data signals to reproduce the bits of the print data signals to operate the printing device.

An embodiment of the invention will now be described by way of example with reference to the drawings in which

Figure 1 is a block diagram of electronic accounting and control circuits and a digital printing device of a franking machine in which signals for controlling the printing device are transmitted serially, and

Figure 2 is a similar block diagram in which signals for controlling the printing device are transmitted in parallel.

Referring to Figure 1 of the drawing, electronic accounting and control circuits for a franking machine are constituted in well known manner by a microprocessor 10 to which required command signals and

postage value signals are input by means of keyboard 11. A display device 12 is provided for the display of information to a user of the franking machine. Non-volatile memory devices 13, 14 are provided for the storage of accounting data. Each memory 13, 14 includes a descending register for storing a value of credit available for use in franking of mail items, an ascending tote register for storing an accumulated value of postage used in franking mail items by the franking machine, an items count register for storing a count of the number of items franked and a high items register to store a count of the number of items franked with a postage charge in excess of a predetermined value. The registers are duplicated in each of the memory devices 13, 14 in order to enable verification of the integrity of stored accounting data.

A digital printing device 34 is provided for printing franking impressions on mail items. The printing device comprises a print head 15 having a plurality of print elements arranged in a line and which can be selectively operated to print a plurality of dots in each of a plurality of print cycles to build up a franking impression line by line in successive print cycles. The print elements may be ink jet devices or thermal print elements of a thermal print head. However other forms of printing device in which elements are operated selectively by means of a string of print data signals may be used. The print elements are connected to corresponding memory locations of a print buffer register 16 into which a string of print data bits is entered serially. The bits of the print data string represent a dot pattern to be printed in a line by the print elements in a single print cycle and for example a binary one may represent a dot to be printed and a binary zero may represent a space in which a dot is not to be printed. When the string of print data has been entered into the buffer register 16, a strobe signal on a line 17 causes operation of the print elements in dependence upon the binary values in the memory locations of the buffer register corresponding to the print elements. The strings of print data signals are input serially to the buffer register 16 on line 18 and are clocked into the register by means of clock signals received from the microprocessor 10 on line 19.

It will be appreciated that unauthorised printing of a franking impression could be effected by applying appropriate strings of print signals to the line 18 while the print elements are strobed by strobe signals on line 17. In order to prevent such unauthorised operation of the printing device the print data signals output by the microprocessor 10 are encrypted prior to transmission to the printing device. The encrypted print data signals are carried by a line 20 and are input to a decryption circuit 21 of the printing device. Decrypted print data signals output from the decryption circuit 21 are input to the print buffer register 16 on line 18. The line 18 connecting the output of the decryption circuit 21 to the input of the buffer register is securely

protected to prevent unauthorised application of signals to the input of the buffer register. Accordingly the encryption circuit is mounted in close proximity to the buffer register so that the connection 18 therebetween is as short as is practicable and preferably is encapsulated. Where practical, the encryption circuit 21 may be physically bonded to the buffer register 16 by encapsulation therewith.

The print data signals output by the microprocessor 10 on line 24 are encrypted by an encryption circuit 22 by logically combining the string of print data signals with the output 28 from a pseudo random signal generator 23. The output 28 of the generator 23 comprises a pseudo random string of binary bits and this is combined in a gate 26 with a string of print data signals output by the microprocessor 10 to produce a corresponding string of encrypted print data signals output from the gate 26 onto line 20. The encrypted print data signals are decrypted by means of the decryption circuit 21 which is identical to the encryption circuit 22 and comprises a pseudo random signal generator 27. The pseudo random string of binary bits output on connection 30 from the generator 27 is combined in a gate 29 with the string of encrypted print data signals received on line 20 to produce at the output of the gate 29 on connection 18 a string of decrypted print data signals corresponding to those output by the microprocessor on line 24 to the encryption circuit 22.

It will be understood that the pseudo random generators 23, 27 are maintained in synchronism by clock signals on the line 19. While the pseudo random generators may be clocked at the bit rate of the print data stream as shown in the drawing, if desired the pseudo random generators may be clocked by clock signals at a rate which is a fraction of the bit rate at which the print data signals are clocked so that each bit from the pseudo random generators of the encryption circuit and decryption circuit would be combined with a number, greater than one, of print data signals in succession.

The encryption circuit 22 may be constituted by circuit components specifically provided to carry out this function as described hereinbefore or if desired the encryption of the print data signals may be effected by the microprocessor 10 operating under a program routine to emulate the operation of such a specific encryption circuit.

Accordingly it will be understood that the print data signals are encrypted and the encrypted print data signals are decrypted by logically combining corresponding pseudo random strings of binary bits with the strings of print data signals and encrypted print data signals respectively, the pseudo random strings of bits being maintained in synchronism with each other.

While hereinbefore there has been described a franking machine in which a single serial string of print

data signals is output by the accounting and control microprocessor 10 on a single line 24, it is to be understood that the invention may also be utilised to encrypt and decrypt print data signals which are output in parallel on a plurality of lines  $24_1 - 24_n$  as shown in Figure 2. Successive bits of print data on each line are logically combined in a plurality of gates  $26_1 - 26_n$ , one for each line  $24_1 - 24_n$ , with successive bits of the pseudo-random string of bits from the generator 23 to produce encrypted parallel print data bits on parallel lines  $20_1 - 20_n$ . These encrypted data bits output from the gates are transmitted by means of the plurality of lines to one input of a plurality of gates  $29_1 - 29_n$  respectively at the printing device 34. The other inputs of the gates  $29_1 - 29_n$  at the printing device receive the pseudo random string of bits from generator 27 to decrypt the encrypted print data signals into print data signals which are input in parallel on lines  $18_1 - 18_n$  to the buffer store 16 of the printing head. The same pseudo random string of bits from generator 23 may be input in common to all the gates  $26_1 - 26_n$  and similarly the same pseudo random string of bits from generator 27 is input in common to all the gates  $29_1 - 29_n$ . However if desired different pseudo random strings may be input to the gates provided that corresponding gates  $26_1 - 26_n$  and  $29_1 - 29_n$  receive the same pseudo random strings. That is to say gates  $26_1$  and  $29_1$  receive the same pseudo random strings, gates  $26_2$  and  $29_2$  receive the same pseudo random strings and so on. It will be understood that, in a similar manner to the secure protection of the single connection 18 of the serial embodiment shown in Figure 1, the plurality of connections  $18_1 - 18_n$  of the embodiment shown in Figure 2 are securely protected from unauthorised access.

The term digital printer used hereinbefore is to be understood to include not only printing devices such as ink jet and thermal printers in which dots are printed selectively at selected positions on mail items to build up required printed impressions but also other forms of printing device in which impressions or visual patterns are formed on mail items by selective operation of a plurality of elements. The operation of the elements may produce dots or other shaped patterns and may for example produce segments of characters required to be formed on the mail items.

### Claims

1. A franking machine including electronic accounting and control circuits (10) and a digital printing device (15) operable by print data signals comprising binary bits generated by the accounting and control circuits to print franking impressions including a representation of a postage charge and means to transmit the print data signals from the accounting and control circuits to the digital

printing device characterised by encryption means (22) at the accounting and control circuits (10) comprising a first generator (23) to generate a first pseudo random string of binary bits and means (19) to logically combine successive bits of the first pseudo random string with bits of the print data signals to produce encrypted print data signals and decryption means (21) at the printing device (15) comprising a second generator (27) to generate a second pseudo random string of binary bits corresponding to said first pseudo random string of binary bits and means (29) to combine successive bits of the second pseudo random string of binary bits with bits of the encrypted print data signals to reproduce the bits of the print data signals to operate the printing device (15).

2. A franking machine as claimed in claim 1 further characterised by an electrical connection (18) connecting an output of the decryption means (21) to the printing device (15) and in that said electrical connection (18) is securely protected to inhibit unauthorised access thereto.
3. A franking machine as claimed in claim 2 further characterised in that the decryption means (21) and the printing device (15) are constructed to inhibit direct access to the electrical connection (18).
4. A franking machine as claimed in any preceding claim further characterised in that the logical combination of the print data signals with the pseudo random string of binary bits in the encryption means (22) is effected by a first logic gate (26).
5. A franking machine as claimed in any preceding claim further characterised in that the logical combination of the encrypted print data signals with the second pseudo random string of binary bits in the decryption means (21) is effected by means of a second logic gate (29).
6. A franking machine as claimed in any preceding claim further characterised in that the accounting and control circuits include a microprocessor (10) operating under control of a program routine to generate the first pseudo random string of binary bits and to logically combine the binary bits of said pseudo random string with binary bits of the print data signals.
7. A franking machine as claimed in any preceding claim further characterised in that the print data signals comprise a plurality of bits in parallel.
8. A franking machine as claimed in claim 7 further

characterised in that the encryption means (22) logically combines parallel bits of the print data signals with the bits of the first pseudo-random string to produce parallel bits of the encrypted data signals and wherein the decryption means (21) logically combines the parallel bits of the encrypted data signals with the bits of the second pseudo-random string.

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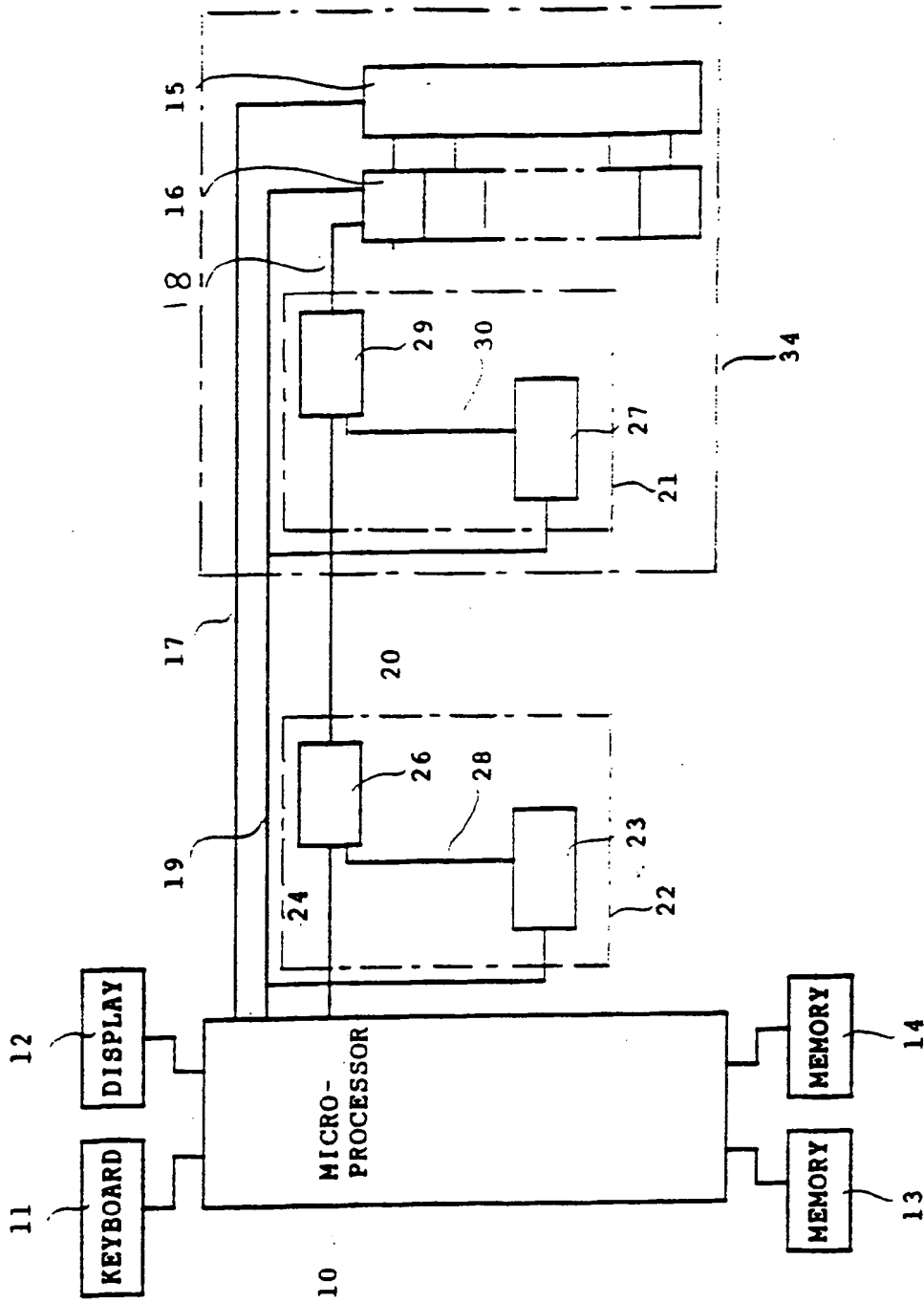


FIGURE 1



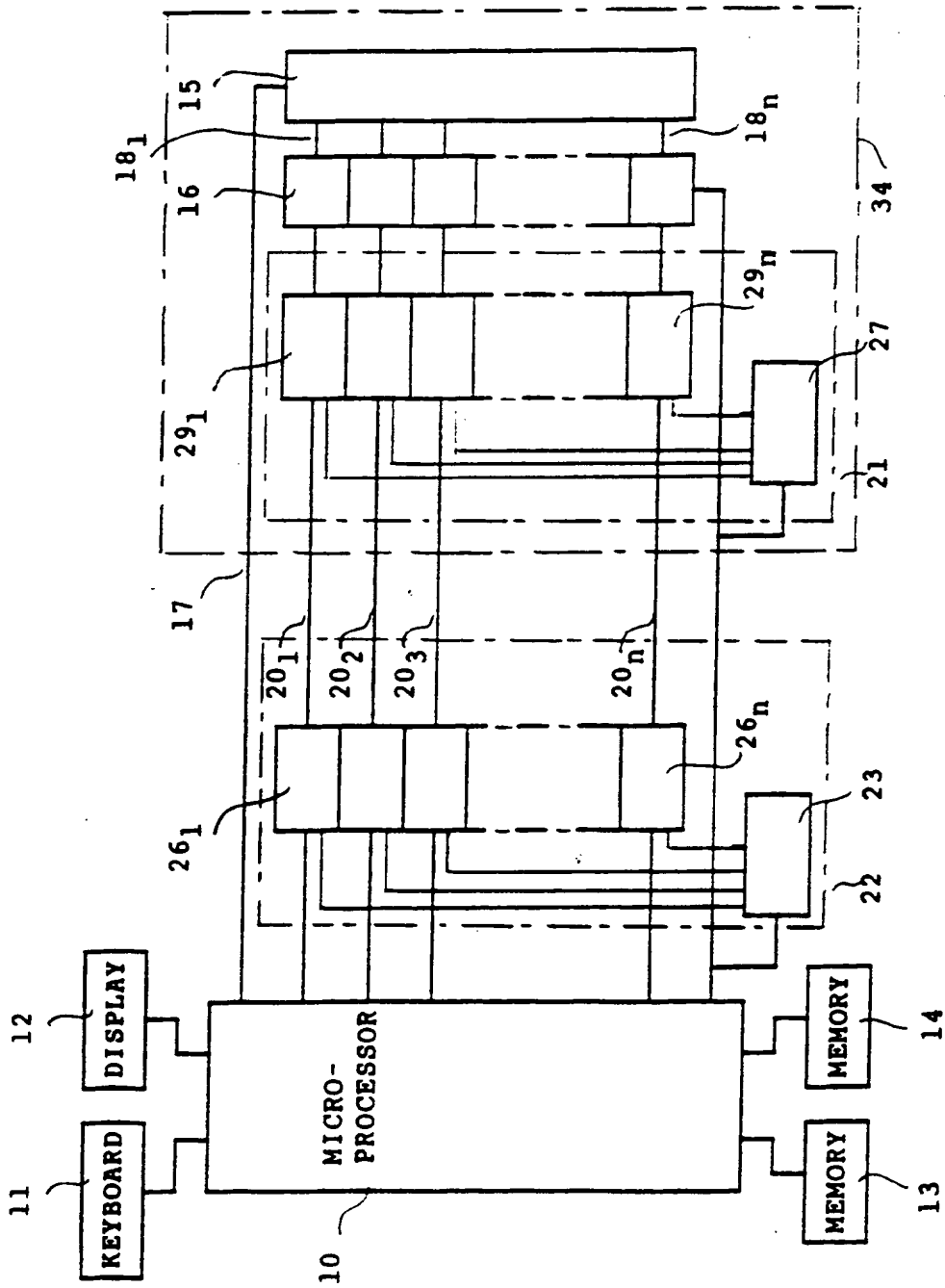


FIGURE 2

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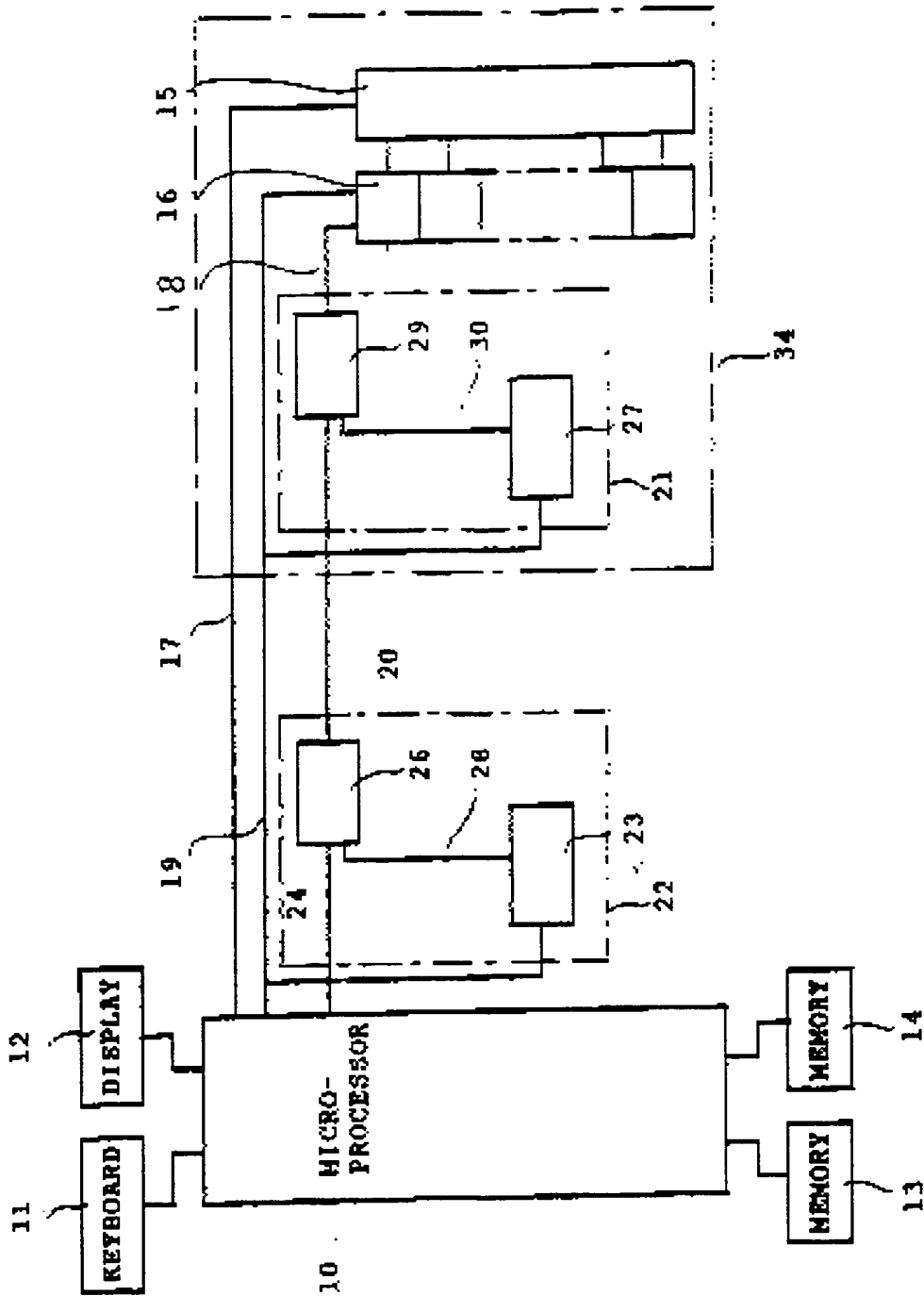


FIGURE 1

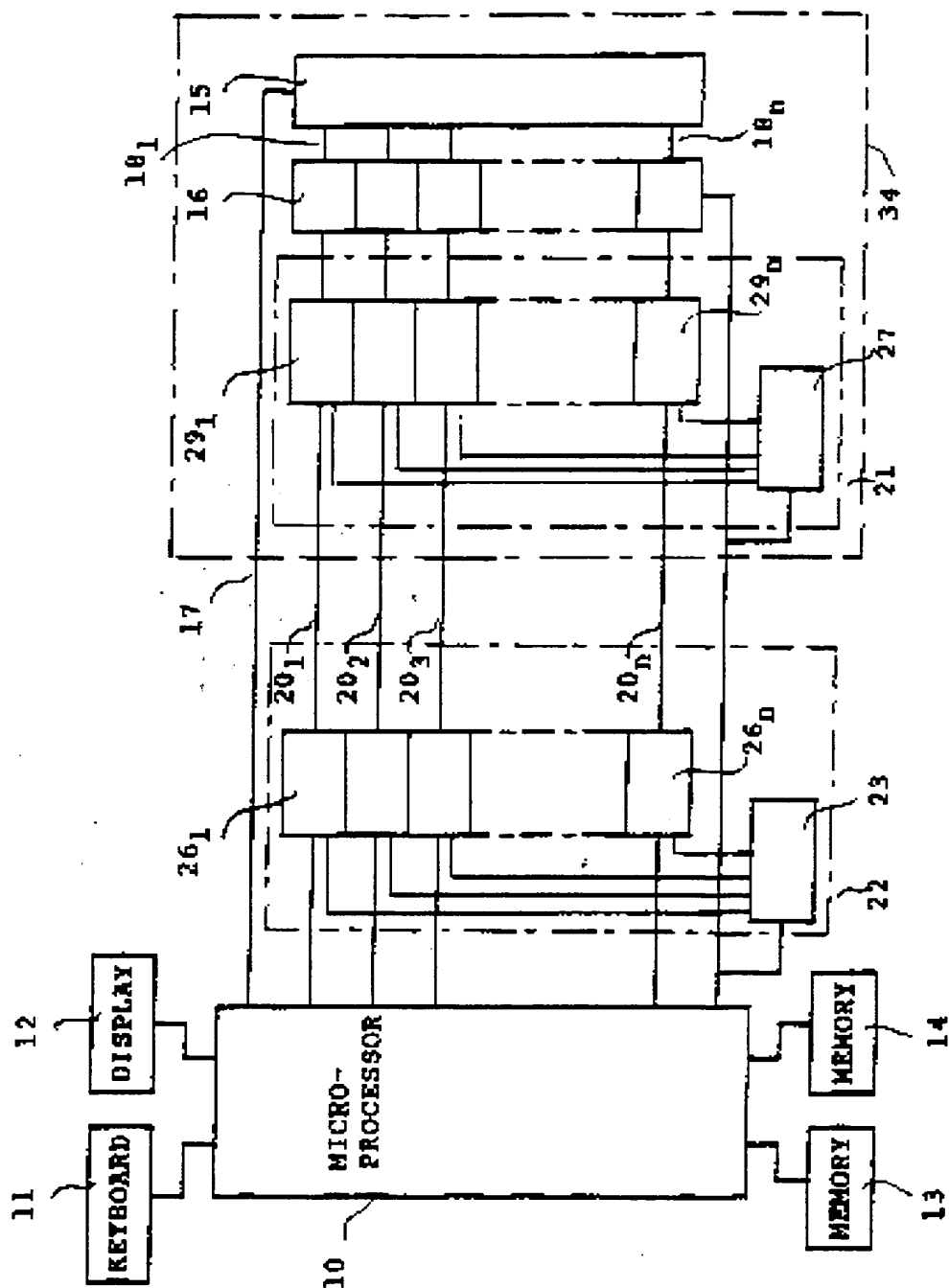


FIGURE 2